

GENERAL REVOCATION AND POWER OF ATTORNEY
CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please revoke any existing Powers of Attorney, if any, in each of the patent applications identified on the attached Appendix, and appoint the attorneys and/or patent agents associated with the following Customer Number to prosecute each of the patent applications identified on the attached Appendix and to transact all business in the Patent and Trademark Office in connection therewith:

Customer Number 57246

Please direct all correspondence in this case to:

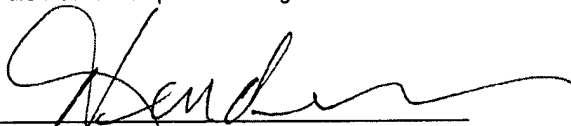
Customer Number 57246

CERTIFICATE UNDER 37 CFR § 3.73(b)

Broadcom Corporation hereby certifies that it is the assignee of the entire right, title and interest in each of the patent applications identified in the attached Appendix by virtue of an assignment from the inventor(s) filed and recorded at the Reel/Frame numbers, as noted in the attached Appendix. To the best of my knowledge and belief, title in each of the patent applications identified on the attached Appendix is in Broadcom Corporation, the assignee.

Pursuant to 37 C.F.R. §3.73(b), I hereby declare that I am empowered to sign this certificate on behalf of Broadcom Corporation, the assignee.

Date 2/20/09

By 
Name: Dee Henderson
Director, Intellectual Property

APPENDIX

DOCKET NUMBER	SERIAL NUMBER	TITLE	FILING DATE	REF/FRAME	INVENTORS
0063-002001	09/246,038	SELF-ADJUSTING ELASTICITY DATA BUFFER WITH PRELOAD VALUE	05-FEB-1999	010009/0882	CASTELLANO, ANDREW J.
0063-003001	09/580,665	APPARATUS FOR A NETWORK HUB TO DIAGNOSE NETWORK OPERATION AND BROADCAST INFORMATION TO A REMOTE HOST OR MONITORING DEVICE	26-MAY-2000	011088/0901	CRAYFORD, IAN; RUNALDUE, THOMAS J.
0063-003002	12/289,194	METHOD AND APPARATUS FOR NETWORK HUB TO DIAGNOSE NETWORK OPERATION AND BROADCAST INFORMATION TO A REMOTE HOST OR MONITORING DEVICE	22-OCT-2008	011088/0901	CRAYFORD, IAN; RUNALDUE, THOMAS J.
0063-004002	10/740,641	APARATUS AND METHOD FOR OPTIMIZING ACCESS TO MEMORY	22-DEC-2003	011262/0400	JOSEPH HERBST, ALLAN FLIPPIN
0063-005002	11/130,187	APPARATUS AND METHOD FOR MANAGING MEMORY IN A NETWORK SWITCH	17-MAY-2005	011265/0994	JOSEPH HERBST
0063-006001	09/599,938	MEMORY MANAGEMENT UNIT FOR A NETWORK SWITCH	23-JUN-2000	011319/0566	JOSEPH HERBST
0063-006002	11/980,511	MEMORY MANAGEMENT UNIT FOR A NETWORK SWITCH	31-OCT-2007	011319/0566	JOSEPH HERBST
0063-007001	09/461,719	STACKED NETWORK SWITCH CONFIGURATION	16-DEC-1999	010469/0806	MOHAN KALKUNTE, SHIRI KADAMBI, SHEKHAR AMBE
0063-007002	10/945,115	STACKED NETWORK SWITCH CONFIGURATION	21-SEP-2004	010469/0806	MOHAN KALKUNTE, SHIRI KADAMBI, SHEKHAR AMBE
0063-007003	10/979,105	MIRRORING IN A STACKED NETWORK SWITCH CONFIGURATION	02-NOV-2004	011671/0456	MOHAN KALKUNTE, ANDERS JOHNSON, SHEKHAR AMBE
0063-008002	11/151,339	METHOD FOR LOAD BALANCING IN A NETWORK SWITCH	14-JUN-2005	010847/0991	SHIRI KADAMBI, MOHAN KALKUNTE, SHEKHAR AMBE
0063-009001	09/528,695	NETWORK SWITCH MEMORY INTERFACE CONFIGURATION	17-MAR-2000	010851/0651	SHIRI KADAMBI, SHEKHAR AMBE
0063-009002	09/527,999	NETWORK SWITCH HAVING A PROGRAMMABLE COUNTER	17-MAR-2000	010856/0255	SHIRI KADAMBI, SHEKHAR AMBE
0063-009003	09/528,001	NETWORK SWITCH STACKING CONFIGURATION	17-MAR-2000	010851/0653	SHIRI KADAMBI, SHEKHAR AMBE
0063-009004	09/527,271	METHOD FOR HANDLING MULICAST PACKETS IN NETWORK SWITCH	17-MAR-2000	010851/0657	SHIRI KADAMBI, SHEKHAR AMBE
0063-009005	10/795,259	METHOD FOR HANDLING MULICAST PACKETS IN NETWORK SWITCH	09-MAR-2004	010851/0657	SHIRI KADAMBI, SHEKHAR AMBE

0063-009006	11/065,562	NETWORK SWITCH HAVING A PROGRAMMABLE COUNTER	25-FEB-2005	010856/0255	SHIRI KADAMBI, SHEKHAR AMBE
0063-010001	09/343,718	METHOD FOR SENDING PACKETS BETWEEN TRUNK PORTS OF NETWORK SWITCHES	30-JUN-1999	010221/0652	SHIRI KADAMBI, SHEKHAR AMBE
0063-010002	09/635,232	METHOD OF SENDING PACKETS BETWEEN TRUNK PORTS OF NETWORK SWITCHES	09-AUG-2000	010221/0652	SHIRI KADAMBI, SHEKHAR AMBE
0063-011002	09/642,104	NETWORK SWITCHING ARCHITECTURE UTILIZING CELL BASED AND PACKET BASED PER CLASS-OF-SERVICE HEAD-OF-LINE BLOCKING PREVENTION	09-AUG-2000	010221/0464	SHIRI KADAMBI, SHEKHAR AMBE
0063-012002	09/983,157	NETWORK SWITCHING ARCHITECTURE WITH FAST FILTERING PROCESSOR	23-OCT-2001	010292/0933	SHIRI KADAMBI, SHEKHAR AMBE
0063-012003	11/219,663	NETWORK SWITCHING ARCHITECTURE WITH FAST FILTERING PROCESSOR	07-SEP-2005	010292/0933	SHIRI KADAMBI, SHEKHAR AMBE
0063-013001	11/698,860	METHOD FOR MANAGING CONJESTION IN A NETWORK SWITCH	29-JAN-2007	010916/0536	SHIRI KADAMBI, MOHAN KALKUNTE, SHEKHAR AMBE
0063-014001	11/433,387	APPARATUS AND METHOD FOR ENABLING VOICE OVER IP SUPPORT FOR A NETWORK SWITCH	15-MAY-2006	011075/0397	KAR-WING EDWARD LOR, MOHAN KALKUNTE, SHEKHAR AMBE
0063-015001	09/642,917	CLUSTER SWITCHING ARCHITECTURE	19-AUG-2000	011031/0515	SHIRI KADAMBI, MOHAN KALKUNTE, SHEKHAR AMBE
0063-015002	11/905,054	CLUSTER SWITCHING ARCHITECTURE	27-SEP-2007	011031/0515	SHIRI KADAMBI, MOHAN KALKUNTE, SHEKHAR AMBE
0063-016001	09/985,763	APPARATUS AND METHOD FOR SORTED TABLE BINARY SEARCH	06-NOV-2001	012299/0022	PAUL KALAPATHY, MIKE JORDA
0063-017003	11/495,739	NETWORK SWITCHING ARCHITECTURE WITH MULTIPLE TABLE SYNCHRONIZATION, AND FORWARDING OF BOTH IP AND IPX PACKETS	31-JUL-2006	010221/0666	SHIRI KADAMBI, SHEKHAR AMBE
0063-018002	10/100,078	HIGH PERFORMANCE SELF BALANCING LOW COST NETWORK SWITCHING ARCHITECURE BASED ON DISTRIBUTED HIERARCHIAL SHARED MEMORY	19-MAR-2002	010221/0448	SHIRI KADAMBI, SHEKHAR AMBE

0063-019002	11/054,435	ADDRESS RESOLUTION SNOOP SUPPORT FOR CPU	10-FEB-2005	010948/0536	GOVIND MALALUR
0063-020002	10/993,491	FLEXIBLE DMA DESCRIPTOR SUPPORT	22-NOV-2004	010948/0552	GOVIND MALALUR
0063-021001	09/685,902	APPARATUS AND METHOD FOR SECURE FIELD UPGRADABILITY WITH UNIQUE FACTORY PROGRAMMED KEY	11-OCT-2000		
0063-021002	09/685,885	APPARATUS AND METHOD FOR SECURE FIELD UPGRADABILITY WITH UNPREDICTABLE CIPHER TEXT	11-OCT-2000	011223/0886	ANDERS JOHNSON
0063-021003	11/545,446	APPARATUS AND METHOD FOR SECURE FIELD UPGRADABILITY WITH UNPREDICTABLE CIPHER TEXT	11-OCT-2006	011223/0886	ANDERS JOHNSON
0063-022001	09/541,151	INTEGRATED ETHERNET SWITCH	31-MAR-2000		
0063-02200	11/245,102	APPARATUS FOR ETHERNET PHY/MAC COMMUNICATION	07-OCT-2005	010933/0517	JOHN K. LENELL
0063-023001	09/709,532	HIGH SPEED FLOW CONTROL METHADODOGY	13-NOV-2000		
0063-024001	09/714,273	TABLE LOOKUP MECHANISM FOR ADDRESS RESOLUTION	17-NOV-2000		
0063-025001	10/083,293	SYSTEM AND METHOD FOR MEASURING THE THICKNESS AND TEMPERATURE OF A CIRCUIT IN A PRINTED CIRCUIT BOARD	27-FEB-2002	012640/0764	JAMES M. KRONROD
0063-025002	10/805,232	METHOD FOR DETERMINING PCB PLANE THICKNESS AND TEMPERATURE	22-MAR-2004		
0063-026001	09/886,859	METHOD AND APPARATUS FOR REGULATING TRANSCEIVER POWER CONSUMPTION FOR A TRANSCEIVER IN A COMMUNICATIONS NETWORK	21-JUN-2001	(ASSIGNMENT: 1- 011930/0483) (ASSIGNMENT: -2- 012926/0062)	HOANG TAN TRAN, MARK BERMAN
0063-027001	09/885,929	SWITCH HAVING EXTERNAL ADDRESS RESOLUTION INTERFACE	22-JUN-2001	011928/0586	MOHAN KALKUNTE, SHEKHAR AMBE, SOMA PULLELA

0063-027002	11/490,181	SWITCH HAVING EXTERNAL ADDRESS RESOLUTION INTERFACE	21-JUL-2006	011928/0586	MOHAN KALKUNTE, SHEKHAR AMBE, SOMA PULLELA
0063-028001	09/877,010	GIGABIT SWITCH WITH FRAME FORWARDING AND ADDRESS LEARNING	11-JUN-2001	011892/0162	SRINIVAS SAMPATH, MOHAN KALKUNTE, SHEKHAR AMBE, SHIRI KADAMBI
0063-028002	09/877,016	GIGABIT SWITCH WITH MULTICAST HANDLING	11-JUN-2001	011892/0088	SHEKHAR AMBE, MOHAN KALKUNTE
0063-028003	09/877,040	GIGABIT SWITCH SUPPORTING MULTIPLE STACKING CONFIGURATIONS	11-JUN-2001	011893/0775	MOHAN KALKUNTE
0063-028004	09/877,011	GIGABIT SWITCH WITH FAST FILTERING PROCESSOR	11-JUN-2001	011892/0080	MOHAN KALKUNTE, SHEKHAR AMBE, SRINIVAS SAMPATH
0063-028005	09/877,014	GIGABIT SWITCH SUPPORTING IMPROVED LAYER 3 SWITCHING	11-JUN-2001	011891/0036	SHEKHAR AMBE, MOHAN KALKUNTE
0063-028006	09/877,069	TRUNKING AND MIRRORING ACROSS STACKED GIGABIT SWITCHES	11-JUN-2001	011893/0969	MOHAN KALKUNTE, SRINIVAS SAMPATH, SHEKHAR AMBE
0063-028007	09/877,013	CASCADING OF GIGABIT SWITCHES	11-JUN-2001	011892/0090	MOHAN KALKUNTE, SHEKHAR AMBE
0063-028008	09/877,022	FLEXIBLE HEADER PROTOCOL FOR NETWORK SWITCH	11-JUN-2001	011893/0824	MOHAN KALKUNTE, SHEKHAR AMBE
0063-028009	11/312,496	GIGABIT SWITCH WITH FRAME FORWARDING AND ADDRESS LEARNING	21-DEC-2005	011892/0162	SRINIVAS SAMPATH, MOHAN KALKUNTE, SHEKHAR AMBE, SHIRI KADAMBI
0063-028010	11/447,915	CASCADING OF GIGABIT SWITCHES	07-JUN-2006	011892/0090	MOHAN KALKUNTE, SHEKHAR AMBE
0063-029001	09/841,189	GIGABIT SWITCH ON CHIP ARCHITECTURE	25-APR-2001	012009/0780	GOVIND MALALUR, SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE
0063-029002	11/490,109	GIGABIT SWITCH ON CHIP ARCHITECTURE	21-JUL-2006	012009/0780	GOVIND MALALUR, SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE

0063-030001	09/931,754	METHOD AND APPARATUS FOR FILTERING PACKETS BASED ON FLOWS USING ADDRESS TABLES	20-AUG-2001	012330/0866	MOHAN KALKUNTE, SHEKHAR AMBE, SHIRI KADAMBI
0063-031001	09/893,972	HARDWARE ASSIST FOR ADDRESS LEARNING	29-JUN-2001	011956/0518	SRINIVAS SAMPATH, MOHAN KALKUNTE
0063-032001	09/883,213	MEMORY MANAGEMENT UNIT ARCHITECTURE FOR SWITCH FABRIC	19-JUN-2001	011916/0574	JAMES BATTLE, DANIEL TAI
0063-032002	09/883,381	SWITCH FABRIC WITH MEMORY MANAGEMENT UNIT FOR IMPROVED FLOW CONTROL	19-JUN-2001	011917/0080	JAMES BATTLE, DANIEL TAI
0063-032003	10/430,424	FRAME FORWARDING IN SWITCH FABRIC	07-MAY-2003	011916/0595	MOHAN KALKUNTE, SHEKHAR AMBE, SRINIVAS AMPATH
0063-032004	11/402,068	SWITCH FABRIC WITH MEMORY MANAGEMENT UNIT FOR IMPORVED FLOW CONTROL	12-APR-2006	011917/0080	JAMES BATTLE, DANIEL TAI
0063-033002	11/440,151	METHODS AND CIRCUITS FOR POWER MANAGEMENT IN A TRANSCEIVER	25-MAY-2006	(ASSIGNMENT 1-010809/0920)	XI CHEN
0063-034001	09/953,881	NETWORK SWITCH HAVING PORT BLOCKING CAPABILITY	18-SEP-2001	(ASSIGNMENT: 2-015571/0985) 012175/0257	MOHAN KALKUNTE, SHEKHAR AMBE, SAM SAMPATH
0063-034001	11/790,906	NETWORK SWITCH HAVING PORT BLOCKING CAPABILITY	27-APR-2007	012175/0257	MOHAN KALKUNTE, SHEKHAR AMBE, SAM SAMPATH
0063-035001	09/955,985	METHOD AND APPARATUS FOR ENABLING L3 SWITCHING BY A NETWORK SWITC IN A STACKING ENVIRONMENT	20-SEP-2001	(ASSINGMENT1-012191/0557)	SHEKHAR AMBE, MOHAN KALKUNTE
0063-036001	09/863,318	METHOD AND APPARATUS PF INTER-CHIP SHARED BY MSSAGE PASSING AND MEMORY ACCESS	24-MAY-2001	(ASSINGMENT2-012182/0868) (ASSINGMENT1-011841/0539)	JASON FAN, MICHAEL SOKOL
0063-036002	10/965,719	SWITCH HAVING FLOW CONTROL MANAGEMENT	18-OCT-2004	(ASSINGMENT2-015571/0985) (ASSINGMENT1-011624/0681)	JIANN-JYH (JAMES) LAY
0063-037002	11/150,152	SWITCH MEMORY MANAGEMENT USING A LINKED LIST STRUCTURE	13-JUN-2005	(ASSINGMENT2-015571/0985) (ASSIGNMENT:1-011815/0976) (ASSIGNMENT:2-012186/0844)	MICHAEL A. SOKOL

				(ASSIGNMENT:3-015571/0985)	
0063-038002	11/889,741	METHOD AND APPARATUS FOR REDUCING CLOCK SPEED AND POWER CONSUMPTION	16-AUG-2007	(ASSIGNMENT:1-011816/0093)	MICHAEL CHANG, MICHAEL A. SOKOL
0063-039001	09/956,020	SWITCH ASSEMBLY HAVING MULTIPLE BLADES IN A CHASSIS	20-SEP-2001	(ASSIGNMENT:2-015571/0985) 012187/0957	MOHAN KALKUNTE, SHEKHAR AMBE
0063-040001	09/943,238	LINKED NETWORK SWITCH CONFIGURATION	31-AUG-2001	(ASSIGNMENT:1-012138/0547)	SHRJIE TZENG
0063-040002	12/219,907	LINKED NETWORK SWITCH CONFIGURATION	30-JUL-2008	(ASSIGNMENT:2-015571/0985)	
0063-041002	11/006,563	LINKED NETWORK SWITCH CONFIGURATION	08-DEC-2004	015571/0985	SHRJIE TZENG
0063-042001	09/241,658	BIT SLICE ARBITER	02-FEB-1999	(ASSIGNMENT: 1-009759/0661)	JUN CAO
				(ASSIGNMENT: 2-010706/0304)	
0063-042002	10/759,454	BIT SLICE ARBITER	20-JAN-2004	(ASSIGNMENT: 3-011661/0313) (ASSIGNMENT: 1-009759/0661)	JUN CAO
				(ASSIGNMENT: 2-010706/0304)	
0063-043003	10/635,465	LOAD BALANCING IN LINK AGGREGATION AND TRUNKING	07-AUG-2003	(ASSIGNMENT: 3-011661/0313) (ASSIGNMENT:1-009772/0091)	DAVID WONG, CHENG-CHUNG SHIH, JUN CAO, WILLIAM DAI
				(ASSIGNMENT:2-010700/0796)	
0063-045001	09/650,260	STARVATION FREE FLOW CONTROL IN A SHARED MEMORY SWITCHING DEVICE	29-AUG-2000	(ASSIGNMENT:3-011660/0827) 011663/0315	YAO-CHING LIU, WILLIAM DAI, JASON CHAO
0063-045002	11/512,259	STARVATION FREE FLOW CONTROL IN A SHARED MEMORY SWITCHING DEVICE	30-AUG-2006	011663/0315	YAO-CHING LIU, WILLIAM DAI, JASON CHAO

0063-046001	09/800,784	METHOD AND APPARATUS FOR HIGH SPEED TABLE SEARCH	08-MAR-2001	011600/0233	DENNIS SUNGIK LEE, MICHAEL VENG-CHONG LAU, PEI-FENG ADRIAN WANG ET AL
0063-046002	10/750,960	METHOD AND APPARATUS FOR HIGH SPEED TABLE SEARCH	05-JAN-2004	011600/0233	DENNIS SUNGIK LEE, MICHAEL VENG-CHONG LAU, PEI-FENG ADRIAN WANG ET AL
0063-047001	09/526,102	METHOD AND APPARATUS FOR SHARED BUFFER PACKET SWITCHING	15-MAR-2000	(ASSIGNMENT: 1-010929/0922) (ASSIGNMENT: 2-011663/0283)	CHUEN-SHEN BERNARD SHUNG
0063-047002	10/703,523	METHOD AND APPARATUS FOR SHARED BUFFER PACKET SWITCHING	10-NOV-2003	ASSIGNMENT: 1-010929/0922) (ASSIGNMENT: 2-011663/0283	CHUEN-SHEN BERNARD SHUNG
0063-048001	09/794,948	SWITCH USING A SEGMENTED RING	28-FEB-2001	011890/0367	JUN CAO, WILLIAM DAI, YONGBUM KIM
0063-049001	09/861,745	METHOD AND APPARATUS FOR PERFORMING A BINARY SEARCH OF AN EXPANDED TREE	22-MAY-2001	011850/0393	JUN CAO, BRANDON CARL SMITH, ERIC NG
0063-049002	10/937,321	METHOD AND APPARATUS FOR PERFORMING A BINARY SEARCH OF AN EXPANDED TREE	10-SEP-2004	011850/0393	JUN CAO, BRANDON CARL SMITH, ERIC NG
0063-050001	11/416,108	METHOD AND APPARATUS FOR PERFORMING DIAGNOSTIC TESTS USING AN ASSISTING TRANSCEIVER	03-MAY-2006	012657/0176	SANG T. BUI
0063-051001	10/082,077	EFFICIENT OPTIMIZATION ALGORITHM IN MEMORY UTILIZATION FOR NETWORK APPLICATIONS	26-FEB-2002	012634/0562	SHIH-HSIUNG NI
0063-052001	09/982,926	ALGORITHM FOR TIME BASED QUEUING IN A NETWORK TRAFFIC ENGINEERING	22-OCT-2001	012282/0262	SHIH-HSIUNG NI
0063-053001	10/083,591	BINARY SEARCH IN FOUR PARALLEL BANKS	27-FEB-2002	(ASSIGNMENT:1-012644/0508) (ASSIGNMENT:2-015537/0940)	JONATHAN LIN, DAVID BILLINGS, MIKE JORDA
0063-054001	10/090,797	POINTER BASED BINARY SEARCH ENGINE AND METHOD FOR USE IN NETWORK DEVICES	06-MAR-2002	012671/0799	JONATHAN LIN, SOMAYAJULU S.K. PULLELA, DAVID BILLINGS
0063-055001	10/083,594	SYSTEM AND METHOD FOR SLOT BASED ARL TABLE LEARNING WITH CONCURRENT TABLE SEARCH USING WRITE SNOOP	27-FEB-2002	012661/0097	JONATHAN LIN, DAVID BILLINGS

0063-056001	10/083,132	SYSTEM AND METHOD FOR SLOT BASED ARL TABLE LEARNING AND CONCURRENT TABLE SEARCH USING RANGE ADDRESS INSERTION BLOCKING	27-FEB-2002	012638/0150	JONATHAN LIN, DAVID BILLINGS
0063-057001	10/098,337	NETWORK INTERFACE USING PROGRAMMABLE DELAY AND FREQUENCY DOUBLER	18-MAR-2002	012705/0612	JONATHAN LIN, YONG JIANG
0063-058001	10/083,291	NETWORK INTERFACE WITH DOUBLE DATA RATE AND DELAY LOCKED LOOP	27-FEB-2002	012643/0136	JONATHAN LIN, YONG JIANG
0063-059001	10/084,365	VOLTAGE MODE DIFFERENTIAL DRIVER AND METHOD	28-FEB-2002	012642/0559	NING LI, JIANN-CHYI (SAM) SHIEH
0063-059002	10/747,310	VOLTAGE MODE DIFFERENTIAL DRIVER AND METHOD	30-DEC-2003	012642/0559	NING LI, JIANN-CHYI (SAM) SHIEH
0063-060001	09/982,794	DATA PATH OPTIMIZATION ALGORITHM	22-OCT-2001	012277/0485	SHIH-HSIUNG NI
0063-061001	10/101,056	WIDE COMMON MODE DIFFERENTIAL INPUT	20-MAR-2002	012714/0560	NING LI, JIANN-CHYI SAM SHIEH
0063-061002	10/804,169	WIDE COMMON MODE DIFFERENTIAL INPUT AMPLIFIER AND METHOD	19-MAR-2004	012714/0560	NING LI, JIANN-CHYI SAM SHIEH
0063-062001	09/970,650	METHOD AND APPARATUS FOR ENABLING ACCESS ON A NETWORK SWITCH	05-OCT-2001	012234/0734	KAR-WING EDWARD LOR
0063-063001	10/173,422	SELECTIVE FLOW CONTROL BASED ON MULTIPLE VIRTUAL CHANNELS	18-JUN-2002	013023/0887	SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE, SANDEEP RELAN, ALLAN CHRISTIE, URI ELZUR ET AL
0063-063002	11/806,427	SELECTIVE FLOW CONTROL BASED ON MULTIPLE VIRTUAL CHANNELS	31-MAY-2007	013023/0887	SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE, SANDEEP RELAN, ALLAN CHRISTIE, URI ELZUR ET AL
0063-064001	10/079,576	ADAPTABLE SWITCH ARCHITECTURE THAT IS INDEPENDENT OF MEDIA TYPES	22-FEB-2002	012618/0914	SHIRI KADAMBI, SHEKHAR AMBE, SANDEEP RELAN
0063-065001	10/173,421	FLOW BASED CONGESTION CONTROL	18-JUN-2002	013022/0227	SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE, SANDEEP RELAN

0063-065002	11/727,614	FLOW BASED CONGESTION CONTROL	27-MAR-2007	013022/0227	SHIRI KADAMBI, SHEKHAR AMBE, MOHAN KALKUNTE, SANDEEP RELAN
0065-066001	10/090,845	AN OPTIMIZED DATA PATH STRUCTURE FOR MULTI-CHANNEL MIB EVENT-GENERATION	06-MAR-2002	012672/0748	SHIH-HSIUNG NI
0063-067001	10/121,712	TITLE NOT MATCHING ROBUST AND SCALABLE DE-SKEW METHOD FOR DATA PATH SKEW CONTROL	15-APR-2002	012796/0045	JOHN MING YUNG CHIANG
0063-067002	11/637,001	ROBUST AND SCALABLE DE-SKEW METHOD FOR DATA PATH SKEW CONTROL	12-DEC-2006	012796/0045	JOHN MING YUNG CHIANG
0063-068001	10/119,074	FLEXIBLE APPARATUS FOR SETTING CONFIGURATIONS USING AN EEPROM	10-APR-2002	012787/0130	WEN-CHENG TSENG, HSIN-MIN YEH
0063-068002	10/805,233	A NETWORK DEVICE HAVING A FLEXIBLE EEPROM FOR SETTING CONFIGURATION SETTINGS	22-MAR-2004	012787/0130	WEN-CHENG TSENG, HSIN-MIN YEH
0063-069001	10/171,573	HIGIG PROTOCOL FOR INTERCONNECTING MODULAR STRUCTURES	17-JUN-2002	013017/0280	SRINIVAS SAMPATH, MOHAN KALKUNTE
0063-070001	10/076,367	METHOD AND APPARATUS FOR FLEXIBLE FRAME PROCESSING AND CLASSIFICATION ENGINE	19-FEB-2002	012603/0620	DENNIS S. LEE, YONGBUM KIM, HYUNGWON KIM
0063-071001	10/180,509	METHOD AND SYSTEM FOR RATE ADAPTATION	27-JUN-2002	013048/0729	DAVID WONG
0063-071002	12/213,832	METHOD AND SYSTEM FOR RATE ADAPTATION	25-JUN-2008	013048/0729	DAVID WONG
0063-071003	10/180,510	SYSTEM AND METHOD FOR ISOLATING NETWORK CLIENTS	27-JUN-2002	013046/0386	DAVID WONG
063071004	10/292,697	SYSTEM AND METHOD OF A MANAGEMENT INFORMATION BASE AUTOCAST IN A DATA NETWORK	13-NOV-2002	013489/0120	DAVID WONG
0063-071005	10/183,637	SELECTIVE EARLY DROP METHOD AND SYSTEM	28-JUN-2002	013052/0274	DAVID WONG

0063-072001	10/090,844	ADAPTIVE FLOW CONTROL METHOD AND APPARATUS	06-MAR-2002	012671/0830	YI-HSIEN HAO
0063-072002	11/020,114	ADAPTIVE FLOW CONTROL METHOD AND APPARATUS	27-DEC-2004	012671/0830	YI-HSIEN HAO
0063-073002	10/351,487	SCALABLE PACKET FILTER FOR A NETWORK DEVICE	27-JAN-2003	013708/0965	SRINIVAS SAMPATH, MOHAN KALKUNTE
0063-074001	10/247,298	IP MULTICAST PACKET REPLICATION PROCESS AND APPARATUS THEREFORE	20-SEP-2002	013310/0978	LAXMAN SHANKAR
0063-075001	10/683,465	FAST AND SLOW PATH MULTIMEDIA LOOKUP METHOD AND APPARATUS	14-OCT-2003	014928/0152	AIDAN O'ROURKE
0063-076002	11/063,868	FAST FLEXIBLE FILTER PROCESSOR BASED ARCHITECTURE FOR A NETWORK DEVICE	23-FEB-2005	013205/0094	SHEKHAR AMBE, SHIRI KADAMBI, SANDEEP RELAN
0063-077002	11/702,578	AN EFFICIENT METHOD TO PARSE SPI-4 DATA STREAMS	06-FEB-2007	013283/0783	NGOK YING CHU, JOHN M. CHIANG
0063-078001	10/269,989	A METHOD AND APPARATUS UTILIZING A TAIL BUS TO SOLVE BACK-TO-BACK DATA BURST PROBLEMS	15-OCT-2002	013389/0353	NGOK YING CHU, JOHN M. CHIANG
0063-079001	10/377,664	FASTPATH IMPLEMENTATION FOR TRANSPARENT LAN SERVICES OVER MPLS	04-MAR-2003	013837/0058	LAXMAN SHANKAR, SHEKHAR AMBE
0063-080002	11/802,386	FLOW CLASSIFICATION RULES COMBINATION	22-MAY-2007	013376/0293	LAXMAN SHANKAR
0063-081001	10/247,299	TWO STAGE EGRESS SCHEDULER	20-SEP-2002	013310/0683	LAXMAN SHANKAR, SHEKHAR AMBE
0063-082001	10/351,520	WEIGHTED FAIR QUEUEING (WFQ) SHAPER	27-JAN-2003	013708/0180	LAXMAN SHANKAR, SHEKHAR AMBE
0063-082002	10/351,492	SHARED WEIGHTED FAIR QUEUEING (WFQ) SHAPER	27-JAN-2003	(ASSIGNMENT: 1-013708/0989) (ASSIGNMENT: 2-013789/0698)	LAXMAN SHANKAR, SHEKHAR AMBE

0063-083001	10/212,211	PACKET FILTERNG BASED ON PORT BIT MAP	06-AUG-2002	013178/0176	SHEKHAR AMBE, SANDEEP KUMAR RELAN
0063-084001	10/358,222	FAST PATH IMPLEMENTATION FOR TRANSPARENT LAN SERVICES USING DOUBLE TAGGING	05-FEB-2003	013743/0344	LAXMAN SHANKAR, SHEKHAR AMBE
0063-085001	10/245,289	PER COS MEMORY PARTITIONING	18-SEP-2002	013299/0871	LAXMAN SHANKAR, SHEKHAR AMBE
0063-085002	11/518,277	PER COS MEMORY PARTITIONING	11-SEP-2006	013299/0871	LAXMAN SHANKAR, SHEKHAR AMBE
0063-086001	10/378,937	FAST PATH IMPLEMENTATION FOR AN UPLINK DOUBLE TAGGING ENGINE	05-MAR-2003	013834/0743	LAXMAN SHANKAR, SHEKHAR AMBE, SONG-HUO YU
0063-087001	10/163,361	AN ACROSS-DEVICE COMMUNICATION PROTOCOL	07-JUN-2002	(ASSIGNMENT: 1-1012978/0123)	SHRJIE TZENG, YI-HSIEN HAO
0063-088001	10/163,363	FAIRNESS SCHEME METHOD AND APPARATUS FOR PAUSE CAPABLE AND PAUSE INCAPABLE PORTS	07-JUN-2002	(ASSIGNMENT: 2-015571/0985)	SHRJIE TZENG, YI-HSIEN HAO
0063-088002	12/222,291	FAIRNESS SCHEME METHOD AND APPARATUS FOR PAUSE CAPABLE AND PAUSE INCAPABLE PORTS	06-AUG-2008	(ASSIGNMENT: 1-012978/0115)	SHRJIE TZENG, YI-HSIEN HAO
0063-089002	11/898,265	DYNAMIC ADJUST MULTICAST DROP THRESHOLD TO PROVIDE FAIR HANDLING BETWEEN MULTICAST AND UNICAST FRAMES	11-SEP-2007	(ASSIGNMENT: 2-015571/0985)	SHRJIE TZENG, YI-HSIEN HAO
0063-091002	10/894,126	SELF-REPAIRING BUILT-IN-SELF-TEST FOR LINKED LIST MEMORIES	20-JUL-2004	(ASSIGNMENT: 1-012985/0559)	HYUNG WON KIM, CHUEN-SHEN SHUNG
0063-092001	10/389,922	IP MULTICAST REPLICATION FOR 4K VLANS	18-MAR-2003	013437/0943	LAXMAN SHANKAR, ANAND ANUPAM
0068-093001	10/632,807	ENTERPRISE WIRELESS LAN SWITCHING SYSTEM	04-AUG-2003	013886/0416	KAR-WING EDWARD LOR, RICHARD MARTIN, ALARABI OMAR HASSEN
0063-093002	12/153,679	ENTERPRISE WIRELESS LAN SWITCHING SYSTEM	22-MAY-2008	014369/0001	KAR-WING EDWARD LOR, RICHARD MARTIN, ALARABI OMAR HASSEN

0063-094001	10/317,170	PROGRAMMABLE PACKET FIELD REPLACEMENT ACTION BASED ON RANGE TECHNIQUE	12-DEC-2002	013571/0151	SANDEEP RELAN
0063-095002	11/907,059	FASTPATH IMPLEMENTATION FOR A DOUBLE TAGGING LOOPBACK ENGINE	09-OCT-2007	013742/0920	LAXMAN SHANKAR, SHEKHAR AMBE
0063-096001	08/114,958	APPARATUS AND METHOD FOR SYNTHESIS OF SIGNALS WITH PROGRAMMABLE PERIODS	31-AUG-1993	(ASSINGMENT 1-006765/0268)	ALISTAIR D. BLACK, THOMAS M. TOBIN
0063-097002	10/916,597	APPARATUS AND METHOD FOR UNILATERAL TOPOLOGY DISCOVERY IN NETWORK MANAGEMENT	12-AUG-2004	(ASSINGMENT 2-008239/0785) (ASSIGNMENT: 1-1010614/0119) (ASSIGNMENT:2-013609/0288)	KIM K. BANKER, CHRISTOPHER ALAN DEL SIGNORE, GAVIN BOWLBY, RICHARD KARL FELDMAN ET AL
0063-098002	10/345,707	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	15-JAN-2003	(ASSIGNMENT:3-013609/0381) (ASSIGNMENT:1-010028/0727) (ASSIGNMENT: 2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098003	10/348,642	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COSE	21-JAN-2003	(ASSIGNMENT:1-010028/0727) (ASSIGNMENT:2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098004	10/349,377	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-JAN-2003	(ASSIGNMENT:1-010028/0727) (ASSIGNMENT:2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098004	10/349,739	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-JAN-2003	(ASSIGNMENT:1-010028/0727) (ASSIGNMENT:2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098006	10/348,721	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-JAN-2003	(ASSIGNMENT: 1-010028/0727) (ASSIGNMENT: 2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098007	10/348,997	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-JAN-2003	(ASSIGNMENT: 1-010028/0727) (ASSIGNMENT: 2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098008	10/348,620	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	21-JAN-2003	(ASSIGNMENT: 1-010028/0727) (ASSIGNMENT: 2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN
0063-098009	12/149,373	FIBRE CHANNEL ARBITRATED LOOP BUFFERLESS SWITCH CIRCUITRY TO INCREASE BANDWIDTH WITHOUT SIGNIFICANT INCREASE IN COST	30-APR-2008	(ASSIGNMENT: 1-010028/0727) (ASSIGNMENT: 2-013609/0300)	ALISTAIR D. BLACK, KURT CHAN

0063-099002	10/684,873	DIGITAL SUBCHANNEL TRANSCIEVER FOR TRANSMITTING DATA	15-OCT-2003	(ASSIGNMENT: 1- 010614/0132) (ASSIGNMENT: 2- 013609/0378)	RICHARD KARL FELDMAN, FARIVAR FARZANEH, MICHAEL TIMOTHY KAUFFMAN
0063-100003	11/505,816	SWITCH ASSISTED FRAME ALIASING FOR STORAGE VIRTUALIZATION	18-AUG-2006	(ASSIGNMENT:1- 012149/0425)	LANI WILLIAM BREWER, JOHN GIFFORD LOGAN
0063-101001	10/874,551	FAST FILTERING PROCESSOR FOR A HIGHLY INTEGRATED NETWORK DEVICE	24-JUN-2004	(ASSIGNMENT: 2- 013609/0464) 015514/0074	JEFF DULL
0063-102001	10/874,574	EQUAL AND WEIGHTED COS MULTIPASS LOAD BALANCING IN A NETWORK DEVICE	24-JUN-2004	(ASSIGNMENT: 1- 015512/0163) (ASSIGNMENT: 2- 017008/0264)	SONG-HUO YU, YIH-CHUAN S. CHEN, JEFF DULL
0063-103001	10/795,282	WEIGHTED FAIR QUEUE IMPLEMENTATION FOR MEMORY MANAGEMENT UNIT OF A NETWORK DEVICE	09-MAR-2004	015078/0378	ERIK ANDERSEN
0063-104001	10/800,731	DATAGRAM REPLICATION IN INTERNET PROTOCOL MULTICAST SWITCHING IN A NETWORK DEVICE	16-MAR-2004	015100/0626	JIMMY S. WONG
0063-105001	10/874,550	SINGLE AND DOUBLE TAGGING SCHEME FOR PACKET PROCESSING	24-JUN-2004	(ASSIGNMENT:1- 015514/0684) (ASSIGNMENT: 2- 016467/0299)	YIH-CHUAN S. CHEN, JEFF DULL
0063-106001	10/780,853	METHOD AND APPARATUS FRO MULTI-CHIP ARL SYNCHRONIZATION TITLE NOT MATCHING	19-FEB-2004	015009/0415	SHRJIE TZENG, CHIUNG-CHI LIN
0063-107001	10/819,980	IPMC REPLICATION	08-APR-2004	015199/0767	KARAGADA RAMARAO KISHORE, CHIEN-HSIEN WU
0063-108001	10/825,654	FLOW CONTROL AND CONGESTION CONTROL IN XGS	16-APR-2004	015225/0422	K.R. KISHORE, MOHAN KALKUNTE, CHIEN-HSIEN WU
0063-109001	10/819,979	DYNAMICALLY SHARED MEMORY IN BCM5695	08-APR-2004	015199/0791	KARAGADA RAMARAO KISHORE, CHIEN-HSIEN WU
0063-110001	10/825,656	ECMP IN XGS	16-APR-2004	015225/0425	MOHAN KALKUNTE, SRINIVAS SAMPATH, KARAGADA RAMARAO KISHORE
0063-111001	10/753,560	SATURATED PACKET AGING MECHANISM	09-JAN-2004	014883/0167	CHIEN-HSIEN WU

0063-112001	10/756,515	ADAPTIVE FLOW CONTROL	14-JAN-2004	014895/0759	JIANN-JYH (JAMES) LAY
0063-112002	12/222,649	ADAPTIVE FLOW CONTROL	13-AUG-2008	014895/0759	JIANN-JYH (JAMES) LAY
0063-113001	10/750,961	SRAM SELF-ADAPTIVE REDUNDANCY CHOICE LOGIC	05-JAN-2004	014868/0213	JIANN-JYH (JAMES) LAY
0063-114001	10/716,529	LINKING LIST QUEUE MANAGEMENT	20-NOV-2003	014730/0595	CHENG-LIANG (ANDREW) HOU
0063-115001	10/694,732	SEGMENTED RATE CONTROL	29-OCT-2003	014648/0370	CHENG-LIANG HOU
0063-116001	10/777,642	THE EFFECTIVE IMPLEMENTATION FOR THE EGRESS CLOCK FOR RGMII	13-FEB-2004	014988/0254	JOHNSON YEN
0063-117001	10/845,360	METHOD OF GLITCH FREE CONTROL OF DDL	14-MAY-2004	015331/0391	YONG H. JIANG
0063-117002	11/511,309	METHOD OF GLITCH FREE CONTROL OF DDL	29-AUG-2006	015331/0391	YONG H. JIANG
0063-118001	10/845,356	NUMERIC AVERAGING CONTROL SYSTEM IN A DDL-DL DESIGN	14-MAY-2004	015319/0787	YONG H. JIANG
0063-119001	10/985,031	MOBID REMAPPING AND PORT SUBSTITUTION	10-NOV-2004	016247/0411	KEVIN CAMERON, GURUMURTHY V.S. YELESWARAPU, MOHAN KALKUNTE, SRINIVAS SAMPATH ET AL
0063-20001	10/811,126	ELASTICITY DATA BUFFER FOR STREAMING DATA	14-MAY-2007	015158/0287	ANDREW CASTELLANO, PINGHUA PETER YANG
0063-120002	11/798,467	ELASTICITY DATA BUFFER FOR STREAMING DATA	14-MAY-2007	015158/0287	ANDREW CASTELLANO, PINGHUA PETER YANG
0063-121001	10/877,975	SYSTEM AND METHOD FOR SIMPLIFYING ANALOG PROCESSING IN A TRANSMITTER INCORPORATING A RANDOMIZATION CIRCUIT	29-JUN-2004	015525/0280	MENG-AN PAN

0063-122003	11/980,463	A CMOS-BASED RECEIVER FOR COMMUNICATIONS APPLICATIONS	31-OCT-2007	015273/0249	JANICE CHIU, HOOMAN DARABI
0063-123001	10/985,033	FIELD PROCESSOR (FP)	10-NOV-2004	(ASSIGNMENT: 1-016243/0384) (ASSIGNMENT: 2-017291/0019)	ERIC A. BADEN, MOHAN KALKUNTE, JOHN J. DULL, VENKATESHWAR BUDUMA
0063-124001	10/806,139	CIRCUIT AND METHOD FOR CONTROLLING GAIN IN AN AMPLIFIER	23-MAR-2004	015126/0788	JANICE CHIU
0063-125001	10/820,812	AN INSENSITIVE TO PROCESS AND POWER VARIATION RECTIFIER FOR WRSSI CIRCUIT	09-APR-2004	015195/0507	JANICE CHIU
0063-12600	10/806,140	AMPLIFIERS AND AMPLIFYING METHODS FOR USE IN TELECOMMUNICATION DEVICES	23-MAR-2004	015126/0839	JANICE CHIU
0063-127001	10/776,531	CHIP ID BASED GIGA TRUNKING TO MAKE 48+4 SYSTEM BETTER PERFORMANCE	12-FEB-2004	014987/0060	SHRJIE TZENG
0063-128001	10/814,225	INDIVIDUALLY PROGRAMMABLE MOST SIGNIFICANT BITS OF VLAN ID	01-APR-2004	015159/0242	JOHNSON YEN, YI-HSIEN HAO
0063-129001	10/953,366	STBC WITH MULTIPLE STREAM IN OFDM FOR WLAN AND ERROR METRICS FOR SOFT DECISION WITH CHANNEL INFORMATION	30-SEP-2004	015859/0476	JOONSUK KIM
0063-130001	10/960,571	INTERFERENCE CANCELLATION OF STBC WITH MULTIPLE STREAM IN OFDM FOR WLAN	08-OCT-2004	015881/0390	JOONSUK KIM
0063-131001	11/136,376	ADAPTIVE CHANNEL WIDTH SENSITIVITY	25-MAY-2005	016607/0317	DANIEL THOMAS DAUM
0063-132001	10/986,141	LINEAR IN-PHASE TRANSMITTER DESIGN	12-NOV-2004	015985/0096	MENG-AN PAN
0063-133001	11/013,913	BIAS TECHNIQUE FOR THE LINEAR IN-PHASE TRANSMITTER	17-DEC-2004	016103/0404	MENG-AN PAN
0063-134001	11/252,562	MIRRORING OF DATA IN A NETWORK DEVICE	19-OCT-2005	017122/0761	VENKATESHWAR BUDUMA, CHIEN-HSIEN WU

0063-135001	11/260,270	CUT-THROUGH SWITCHING IN A NETWORK SWITCH	28-OCT-2005	017157/0477	CHIEN-HSIEN WU
0063-136001	11/272,131	RATE LIMITING AND MINIMUM AND MAXIMUM SHAPING IN A NETWORK DEVICE	14-NOV-2005	017231/0242	BRUCE H. KWAN, CHIEN-HSIEN WU
0063-137001	11/214,989	GENERAL FLEXIBLE PACKET MODIFICATION ENGINE	31-AUG-2005	016946/0474	ERIC NG, WILLIAM DAI
0063-138001	11/214,990	FLEXIBLE PACKET MODIFICATION ENGINE FOR A NETWORK DEVICE	31-AUG-2005	016946/0137	ERIC NG
0063-139001	11/078,508	OF BOOKKEEPING MEMORY IN A SEARCH ENGINE	14-MAR-2005	016387/0570	BRANDON CARL SMITH, JUN CAO
0063-140001	11/078,483	PRELEARNING OF VALUES WITH LATER ACTIVATION OF VALUES	14-MAR-2005	016384/0711	BRANDON CARL SMITH, JUN CAO
0063-141001	11/078,484	TIMESTAMP METERING AND ROLLOVER PREVENTION	14-MAR-2005	016384/0653	BRANDON CARL SMITH, JUN CAO
0063-142001	11/081,057	PROGRAMMABLE METERING BEHAVIOR BASED ON TABLE LOOKUP	16-MAR-2005	016388/0019	BRANDON CARL SMITH, JUN CAO
0063-143001	11/165,498	LAYOUT ARCHITECTURE FOR EXPANDABLE NETWORK DEVICE	24-JUN-2005	016729/0186	SONG-HUO YU, DENNIS SUNGIK LEE
0063-144001	11/081,644	POWERFUL AND EXPANDABLE PIPELINE ARCHITECTURE FOR A NETWORK DEVICE	17-MAR-2005	016391/0653	BRANDON CARL SMITH, JUN CAO
0063-145001	11/084,482	DYNAMIC TABLE SHARING OF MEMORY SPACE WITHIN A NETWORK DEVICE	21-MAR-2005	016401/0304	BRANDON CARL SMITH, JUN CAO
0063-146001	11/341,643	DYNAMIC FILTER PROCESSOR KEY GENERATION BASED ON PACKET TYPE	30-JAN-2006	017506/0850	SONG HUO YU
0063-147001	11/340,479	FILTER BASED RANGE CHECK IN A NETWORK DEVICE	27-JAN-2006	017516/0563	SONG HUO YU

0063-148001	11/357,189	PASSING VALUES THROUGH A MEMORY MANAGEMENT UNIT OF A NETWORK DEVICE	21-FEB-2006	017920/0636	SONG-HUO YU, WILLIAM DAI, JUN CAO
0063-149001	11/165,495	TWO-STAGE PARSER FOR A NETWORK DEVICE	24-JUN-2005	016727/0083	DENNIS SUNGIK LEE, SONG-HUO YU
0063-150001	11/154,585	PACKET ID	17-JUN-2005	016702/0351	DENNIS SUNGIK LEE, BRANDON CARL SMITH
0063-151001	11/154,827	MULTI-PART PARSING IN A NETWORK DEVICE	17-JUN-2005	016704/0752	DENNIS SUNGIK LEE
0063-152001	11/151,429	IP CHECKSUM	14-JUN-2005	016688/0593	DENNIS SUNGIK LEE, ERIC NG
0063-153001	11/341,650	INGRESS HANDLING OF DATA IN A NETWORK DEVICE	30-JAN-2006	017522/0048	SONG HUO YU
0063-154001	11/340,586	TABLE SEARCHING TECHNIQUES IN A NETWORK DEVICE	27-JAN-2006	017517/0476	SONG-HUO YU
0063-155001	11/341,487	LAYER TWO AND LAYER THREE VIRTUAL PRIVATE NETWORK SUPPORT IN A NETWORK DEVICE	30-JAN-2006	017521/0880	SONG HUO YU
0063-156001	11/289,371	FAST FILTER PROCESSOR METERING AND CHAINING	30-NOV-2005	017310/0078	MOHAN KALKUNTE, ERIC A. BADEN
0063-157001	11/289,499	MULTICAST TRUNKING IN A NETWORK DEVICE	30-NOV-2005	017308/0835	MOHAN KALKUNTE, VENKATESHWAR BUDUMA
0063-158001	11/063,968	DDR INTERFACE RECEIVE LOGIC	24-FEB-2005	016338/0522	SUDHANSHU JAIN
0063-159001	11/063,969	PREDICTION OF OPTIMAL SAMPLING JOINT	24-FEB-2005	016338/0513	SUDHANSHU JAIN
0063-160001	11/090,143	CPU TRANSMISSION OF UNMODIFIED PACKETS	28-MAR-2005	016430/0504	ANUPAM ANAND, JOHN JEFFREY DULL

0063-161001	11/100,537	PIPELINE ARCHITECTURE OF A NETWORK DEVICE	07-APR-2005	016459/0420	ANUPAM ANAND, JOHN JEFFREY DULL, ERIC A. BADEN, MICHAEL J. BOWES
0063-162001	11/082,975	A METHOD AND APPARATUS FOR DIRECT MEMORY ACCESS BASED ON CLASS-OF-SERVICE	18-MAR-2005	(ASSIGNMENT: 1-017147/0647)	VAMSI TATAPUDI, NITHYANANDA MIYAR
0063-163001	11/082,976	DIRECT-MEMORY ACCESS FOR CONTENT ADDRESSABLE MEMORY	18-MAR-2005	(ASSIGNMENT: 2-016394/0102) (ASSIGNMENT: 1-016395/0269)	VAMSI TATAPUDI, SHASHI S. MATH
0063-164001	11/090,110	FORCED BUBBLE INSERTION SCHEME	28-MAR-2005	(ASSIGNMENT: 2-018192/0528) 016429/0541	ANUPAM ANAND, CHIEN-HSIEN WU, SAMIR K. SANGHANI
0063-165001	11/060,657	WEIGHTED FAIR QUEUING RELATIVE BANDWIDTH-SHARING	18-FEB-2005	016310/0829	ERIK ANDERSEN
0063-166001	11/060,492	XQ DYNAMIC SHARING	18-FEB-2005	016312/0341	WEITONG CHUANG
0063-167001	11/060,490	XQ DYNAMIC COLOR THRESHOLD	18-FEB-2005	016312/0367	ERIK ANDERSEN
0063-168001	11/063,884	MEMORY SYSTEM SELF-HEALING	23-FEB-2005	016338/0073	ERIK ANDERSEN
0063-169002	11/819,512	MCU THROTTLE AND ERROR RECOVERY	27-JUN-2007	016312/0385	WEITONG CHUANG, ERIK ANDERSEN
0063-170001	11/289,368	HIGH SPEED TRUNKING IN A NETWORK DEVICE	30-NOV-2005	017304/0166	MOHAN KALKUNTE
0063-171001	11/289,369	NETWORK FOR SUPPORTING ADVANCE FEATURES ON LEGACY COMPONENTS	30-NOV-2005	017278/0393	MOHAN KALKUNTE, GURUMURTHY YELESWARAPU, PUNEET AGARWAL, SONG-HUO YU ET AL
0063-172001	11/289,366	MIRRORING IN A NETWORK DEVICE	30-NOV-2005	(ASSIGNMENT: 1-017294/0387) (ASSIGNMENT: 2-017654/0205)	MOHAN KALKUNTE, VENKATESHWAR BUDUMA, PUNEET AGARWAL, GURUMURTHY YELESWARAPU, SONG-HUO YU
0063-173001	11/289,370	VLAN TRANSLATION IN A NETWORK DEVICE	30-NOV-2005	017274/0677	MOHAN KALKUNTE, VENKATESHWAR BUDUMA, SONG-HUO YU, GURUMURTHY YELESWARAPU

0063-174001	11/289,497	UNICAST TRUNKING IN A NETWORK DEVICE	30-NOV-2005	017308/0726	MOHAN KALKUNTE, JOHN JEFFREY DULL, BRUCE H. KWAN, VENKATESHWAR BUDUMA
0063-175001	11/289,687	POLICY BASED ROUTING USING FAST FILTER PROCESSOR	30-NOV-2005	017308/0520	MOHAN KALKUNTE, VENKATESHWAR BUDUMA, ERIC A. BADEN
0063-176001	11/259,041	INTELLIGENT FABRIC CONGESTION DETECTION APPARATUS AND METHOD	27-OCT-2005	017149/0180	PUNEET AGARWAL, BRUCE H. KWAN
0063-177001	11/259,095	INTELLIGENT CONGESTION FEEDBACK APPARATUS AND METHOD	27-OCT-2005	017149/0359	BRUCE H. KWAN, PUNEET AGARWAL
0063-178001	11/260,232	SERVICE AWARE FLOW CONTROL	28-OCT-2005	017153/0136	BRUCE H. KWAN, EUGENE N. OPSASNICK, PUNEET AGARWAL
0063-178002	11/260,269	DYNAMIC THRESHOLD MECHANISM AND ERROR MANAGEMENT	28-OCT-2005	017157/0532	BRUCE H. KWAN, PUNEET AGARWAL, EUGENE N. OPSASNICK
0063-179001	11/172,799	METHOD AND APPARATUS FOR UNIFIED WIRED AND WIRELESS SWITCHING	05-JUL-2005	016754/0821	PUNEET AGARWAL, YOOK-KHAI CHEOK
0063-180001	11/444,373	COMPACT PACKET OPERATION DEVICE AND METHOD	01-JUN-2006	017958/0704	SHRJIE TZENG
0063-182001	11/099,539	DYNAMIC CONNECTIVITY DETERMINATION	06-APR-2005	016447/0796	BRIAN BAIRD, DANIEL TALAYCO
0063-183001	11/099,474	HIGIG AUTOTRUNKING	06-APR-2005	016455/0377	BRIAN BAIRD, DANIEL TALAYCO
0063-184001	11/408,064	GROUP DRIVER	21-APR-2006	017875/0865	DANIEL TALAYCO
0063-185001	11/156,700	USE OF METADATA FOR SEAMLESS UPDATES	21-JUN-2005	016715/0003	CURTIS MCDOWELL
0063-186001	11/429,288	STACKING INTO BCMX	08-MAY-2006	017876/0602	DANIEL TALAYCO, BRIAN BAIRD, ALLAN CHRISTIE

0063-188001	11/429,289	API INTERFACE-MAKING DISPATCH TABLES TO MATCH API ROUTINES TITLE NOT MATCHING	08-MAY-2006	0177877/0566	DANIEL TALAYCO, BRIAN BAIRD
0063-189001	11/429,287	ASYNCHRONOUS EVENT NOTIFICATION	08-MAY-2006	017876/0573	DANIEL TALAYCO, BRIAN BAIRD
0063-190001	11/200,007	METHOD FOR COMMUNICATION BETWEEN PROCESSORS	10-AUG-2005	016828/0611	BRIAN BAIRD, DANIEL TALAYCO
0063-192001	11/497,293	REQUEST PROCESSING BETWEEN FAILURE WINDOWS	02-AUG-2006	018128/0887	PETER MICHELS
0063-193001	11/507,670	TIME MULTIPLEXING LOGIC IN PHYSICAL DESIGN DOMAIN FOR MULTIPLE INSTANTIATION	22-AUG-2006	018219/0342	PETER MICHELS
0063-194001	11/132,280	DIGITAL POWER ON RESET CONTROLLER	19-MAY-2005	016582/0706	JIUNN-MING JU, CHENG-LIANG HOU
0063-195001	11/504,634	PACKET ATTRIBUTE BASED PRIORITIZATION	16-AUG-2006	018185/0022	PETER MICHELS
0063-196001	11/330,187	DYNAMIC PORT FAILOVER	12-JAN-2006	017449/0472	ZHENG WANG, PUNEET AGARWAL
0063-197001	11/480,844	METHOD AND APPARATUS FOR CALCULATING S-FLOW	06-JUL-2006	(ASSIGNMENT: 1-018042/0021 (ASSIGNMENT: 2-019012/0978	MOHAN KALKUNTE
0063-198001	11/189,847	DYNAMIC ALLOCATION OF AGGREGATE BUFFER	27-JUL-2005	016823/0452	KWOK FAI TSANG
0063-199001	11/285,105	METHOD AND APPARATUS FOR DYNAMICALLY CONFIGURING A GENERIC PROCESSING MODULE	23-NOV-2005	017276/0430	VAMSI M. TATAPUDI, ANIRBAN BANERJEE
0063-20001	11/412,861	A SUPPLY VOLTAGE SELECTOR	28-APR-2006	017810/0637	NING LI
0063-201001	11/452,284	LOOP DETECTION FOR A NETWORK DEVICE	14-JUN-2006	017998/0520	SHRJIE TZENG

0063-202001	11/258,124	DYNAMIC TRANSACTION QUEUE SYSTEM	26-OCT-2005	017147/0015	ERIK ANDERSEN, WEITONG CHUANG
0063-203001	11/396,617	HIGH SPEED TRANSMISSION PROTOCOL	04-APR-2006	017715/0132	WILLIAM DAI
0063-204001	11/396,618	APPARATUS AND METHOD FOR IMPLEMENTING MULTIPLE HIGH SPEED SWITCHING FABRICS IN AN ETHERNET RING TOPOLOGY	04-APR-2006	017755/0534	WILLIAM DAI
0063-205001	11/396,619	APPARATUS AND METHOD FOR EXTENDING FUNCTIONS FROM A HIGH END DEVICE TO OTHER DEVICES IN A SWITCHING NETWORK	04-APR-2006	017754/0814	WILLIAM DAI
0063-206001	11/642,554	HIERARCHICAL QUEUE SHAPING	21-DEC-2006	018736/0049	BRUCE H. KWAN, PUNEET AGARWAL, ASAD KHAMISY
0063-207001	11/407,279	CONTENT AWARE APPARATUS AND METHOD	20-APR-2006	017795/0641	ERIC A. BADEN
0063-208001	11/410,885	DIAGNOSTIC MECHANISM FOR LAYER 2 AND LAYER 3 NETWORKS	26-APR-2006	017808/0146	ZHENG WANG
0063-209001	11/324,223	METHOD AND APPARATUS FOR DYNAMICALLY CONFIGURING REGISTERS BY A GENERIC CPU MANAGEMENT INTERFACE	04-JAN-2006	017438/0140	VAMSI M. TATAPUDI, SUNDARESAN KUMBAKONAM
0063-210001	11/324,222	METHOD AND APPARATUS FOR DYNAMICALLY CONFIGURING HARDWARE RESOURCES BY A GENERIC CPU MANAGEMENT INTERFACE	04-JAN-2006	017438/0147	VAMSI M. TATAPUDI, ANIRBAN BANERJEE
0063-211001	11/594,743	PROCESSING OF MULTIPLE CELLS IN A NETWORK DEVICE WITH TWO READS AND TWO WRITES IN ONE CLOCK CYCLE	09-NOV-2006	018571/0281	CHIEN-HSIEN WU, YOOK-KHAI CHEOK, EUGENE OPSASNICK
0063-212001	11/594,744	SCHEDULING OF DATA TRANSMISSION WITH MINIMUM AND MAXIMUM SHAPING OF FLOWS IN A NETWORK DEVICE	09-NOV-2006	018571/0345	CHIEN-HSIEN WU, BRUCE KWAN, PHILIP CHEN
0063-213001	11/598,116	INTERLEAVED PROCESSING OF DROPPED PACKETS IN A NETWORK DEVICE	13-NOV-2006	018569/0274	EUGENE OPSASNICK
0063-214001	11/583,155	CELL COPY COUNT HAZARD DETECTION	19-OCT-2006	018445/0458	VAMSI M. TATAPUDI, CHIEN-HSIEN WU, PHILIP CHEN

0063-215001	11/589,164	FIELD PROGRAMMABLE MEMORY REPAIR FOR EPROM	30-OCT-2006	018483/0436	QING LIN, KIRAN DASOJU
0063-216001	11/592,258	MULTIPLE MODE CAM'S; DOUBLE AND TRIPLE WIDE CAM'S	03-NOV-2006	018511/0527	MICHAEL H. LAU
0063-217001	11/593,103	CACHE-BASED CELL FREE ADDRESS POOL	06-NOV-2006	018522/0492	VAMSI M.TATAPUDI CHIEN- HSIEN WU YUAN LU,
0063-218001	11/595,978	BUFFER MANAGEMENT AND FLOW CONTROL MECHANISM INCLUDING PACKET-BASED DYNAMIC THRESHOLDING	13-NOV-2006	018600/0100	EUGENE OPSASNICK

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

[Insert
ASSIGNEE's
Name(s)
Address(es)]

Broadcom Corporation
16215 Alton Parkway
Irvine, California 92618-3616

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

[Title of
Invention]

HIGH SPEED FLOW CONTROL METHODOLOGY

[*If the assignment
is being filed
after the filing of the
application, this
section must be
completed]

for which application for Letters Patent of the United States was executed on even date herewith unless otherwise indicated below:

*filed on _____, Serial No. _____

(Arent Fox Kintner Plotkin & Kahn is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

and all Letters Patent of the United States to be obtained therefor on said application or any continuation, division, renewal, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with application and any continuing, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, division, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

[Signature(s)
of Assignor(s)]

Michael J. Bowes
(SIGNATURE)

Michael J. BOWES
(TYPE NAME)

X 11/8/00
(DATE)

(SIGNATURE)

(TYPE NAME)

(DATE)

(SIGNATURE)

(TYPE NAME)

(DATE)

(SIGNATURE)

(TYPE NAME)

(DATE)

(SIGNATURE)

(TYPE NAME)

(DATE)

NO LEGALIZATION REQUIRED